Docket No.

243154US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Hidehiro TAKATA

SERIAL NO: New Application

GAU:

FILED:

Herewith

EXAMINER:

FOR:

DATA PROCESSING SYSTEM

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR .

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☐ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Marvin J. Spivak

Registration No. 24,913

C. Invin McClelland Registration Number 21,124

Customer Number

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03)

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	Hidehiro TAKATA, et al., "Physical Design Methodology for On-Chip 64-Mb DRAM MPEG-2 Encoding with a Multime Processor", IEICE TRANSACTIONS ON ELECTRONICS, Vol. E85-C, No. 2, February 2002, pgs. 368-374 Kouichi Yamguchi, et al., "2.5 GHz 4-phase Clock Generator with Scalable and No Feedback Loop Architecture" 200 INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE, ISSCC 2001/SESSION 25/CLOCK GENERATION AND DISTRIBUTION/25.4, pgs. 398-399 & 326-327							•	
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	Thucydides Xanthopoulos, et al., "The Design and Analysis of the Clock Distribution Network for a 1.2 GHz Alpha Microprocessor" 2001 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE, ISSCC 2001/SESSION 25/CLOCK GENERATION AND DISTRIBUTION/25.6, pgs.402-403 & 330-331							sheet(s) attached	
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